



## Best Available Copy

EAST - (default.wsp 1)

File View Edit Tools Window Help

Drafts Pending Active

- L1: (871) pulse and (multipulse or multi-pulse)
- L2: (265) 1 and (correlation or convolution)
- L3: (38) 1 and (correlation or convolution) and zeros
- L4: (1) 1 and (correlation or convolution) same zeros
- L5: (69) 1 and (correlation or convolution) same (zero or non-zero or nonzero)
- L6: (17) mp-mlq

Failed Saved Favorites Tagged (0) UDC Queue Trash

Search Set Browse Queue Clear

DBs USPAT

Default Operator: OR

Highlight all bitmaps (check)

mp-mlq

BRD term Refine View Text HTML

U	Document ID	Issue Date	Pages	Title	Current DB	Current XRef	Retrieval C	Inventor	S	C	P	Q	W	US
1	<input type="checkbox"/> US 6765931 B1 20040720 125	Gateway with voice	370/493	370/285			Rabenko, Theodore F. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
2	<input type="checkbox"/> US 6757367 B1 20040629 39	Packet based network exchange with rate	379/90.01	379/100.01; 379/100.17			Nicol, Jordan James	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
3	<input type="checkbox"/> US 6754203 B2 20040622 17	Method and program product for organizing data into	370/352	370/353; 370/516			Wah, Benjamin Wan-Sang et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
4	<input type="checkbox"/> US 6738733 B1 20040518 7	G.723.1 audio encoder	702/189	704/219			Tian, Wenshun	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
5	<input type="checkbox"/> US 6724416 B1 20040420 16	Image transceiving telephone with integrated digital	348/14.02	348/14.01; 348/14.13			Liu, Jianxin	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
6	<input type="checkbox"/> US 6687668 B2 20040203 13	Method for improvement of G.723.1 processing time and	704/223	704/219; 704/222			Kim, Jeong Jin et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
7	<input type="checkbox"/> US 6636506 B1 20031021 10	Internet telephone system and method therefor	370/356	370/352			Fan, Yuan-Neng	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
8	<input type="checkbox"/> US 6584110 B1 20030624 24	Voice gateway and route selection	370/401	370/352			Mizuta, Yasuhiro et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
9	<input type="checkbox"/> US 6549587 B1 20030415 74	Voice and data exchange over a packet based network with	375/326	375/324; 375/354;			Li, Henry	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
10	<input type="checkbox"/> US 6504838 B1 20030107 83	Voice and data exchange over a packet based network with increased port density	370/352	370/516			Kwan, Kenny C.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				
11	<input type="checkbox"/> US 6445697 B1 20020903 10	Audio codec reselection for increased port density	370/357	370/352; 370/401;			Fenton, James L.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US				

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Drafts Pending Active L1: (0) "multiply by zero" L2: (39) multiply adj zero Failed Saved Favorites Tagged (0) UDC Queue Trash

Search Browse Queue Clear DBs USPAT Default operator: OR

multiply adj zero

Prints Highlight all hits in one initially

Print Form GBR BMP Image Text HTML

U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	g	c	p	s	3	4
1	<input type="checkbox"/>	US 6700515 B2	20040302	20	Digitizer apparatus and semiconductor testing	341/120			Asami, Koji	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	US 6580770 B2	20030617	14	Information regenerating apparatus and information	375/341	375/326; 375/355		Imanaka, Yoshifumi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	US 6507626 B1	20030114	18	Bandpass phase tracker that automatically samples at	375/321	329/357; 329/361;		Limberg, Allen LeRoy	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input type="checkbox"/>	US 6487262 B1	20021126	11	Synchronization and downconversion in TDM/TDMA	375/356	375/358; 375/373		Moulton, Grant E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	US 6434699 B1	20020813	24	Encryption processor with shared memory interconnect	713/168	380/255; 380/28;		Jones, David E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input type="checkbox"/>	US 6379119 B1	20020430	16	Hybrid electric and hydraulic actuation system	417/22	264/40.1		Truninger, Rolf	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input type="checkbox"/>	US 6301599 B1	20011009	11	Multiplier circuit having an optimized booth	708/628	708/629		Chehraz, Farzad et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
8	<input type="checkbox"/>	US 6292194 B1	20010918	67	Image compression method to reduce pixel and texture	345/582			Powell, III, William Chambers	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
9	<input type="checkbox"/>	US 6151179 A	20001121	18	Signal processing circuit with feedback extracted from	360/46	360/51		Poss, Joe Martin	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
10	<input type="checkbox"/>	US 6088800 A	20000711	26	Encryption processor with shared memory interconnect	713/189	380/28; 380/33;		Jones, David E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
11	<input type="checkbox"/>	US 6005730 A	19991221	18	Signal error generating circuit for an analog signal	360/51	360/46		Poss, Joe Martin	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

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